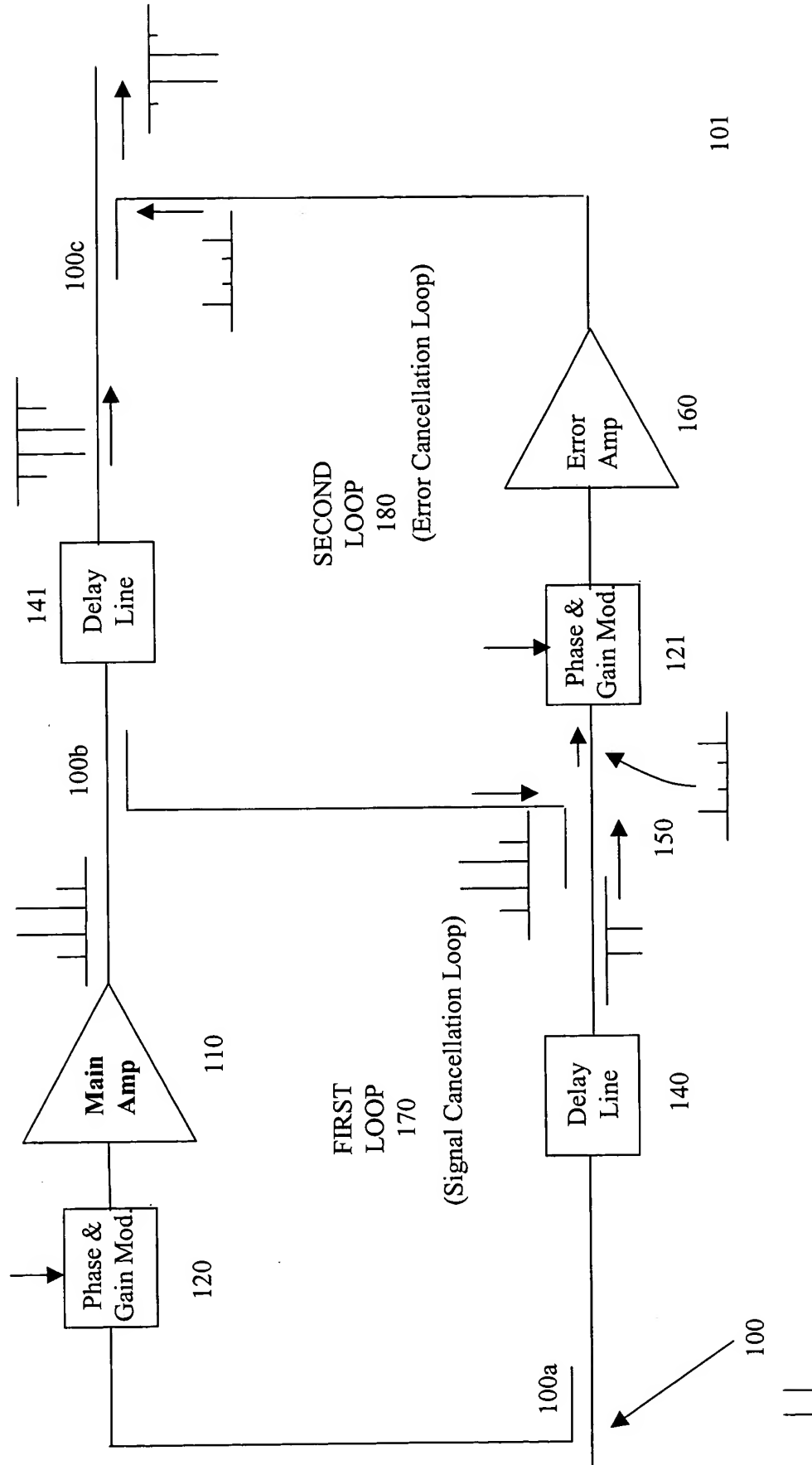


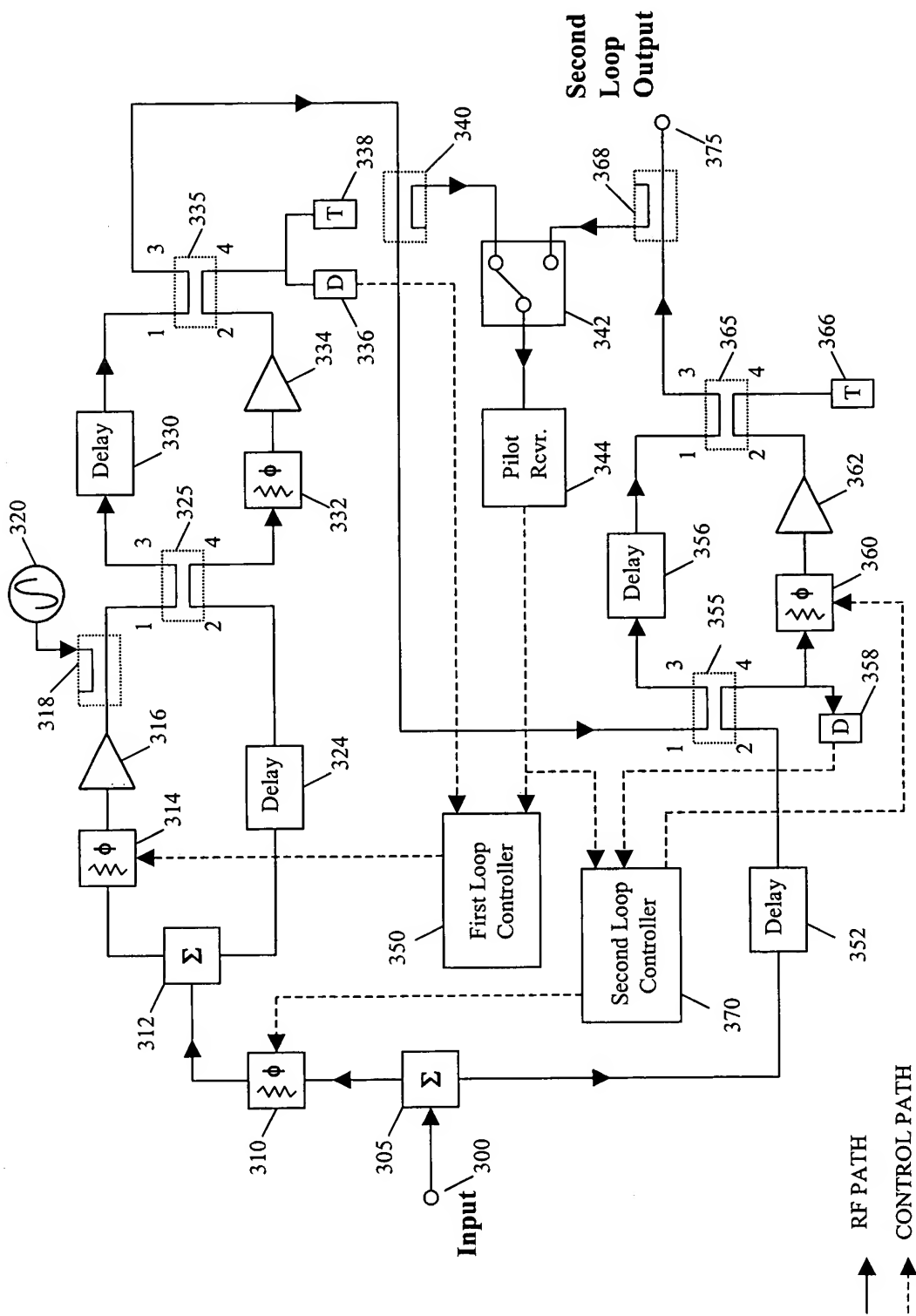


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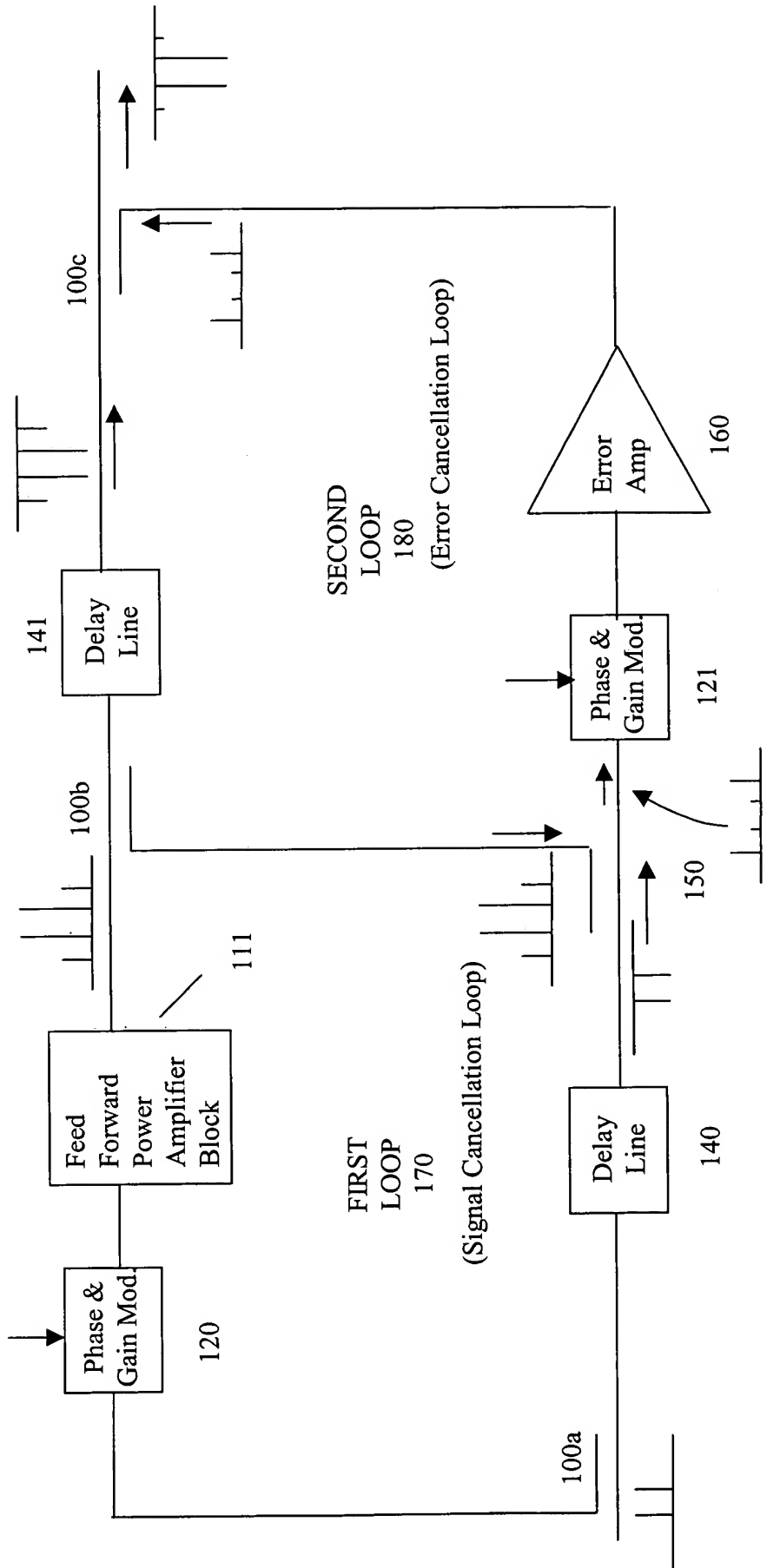
PRIOR ART

FIG. 1



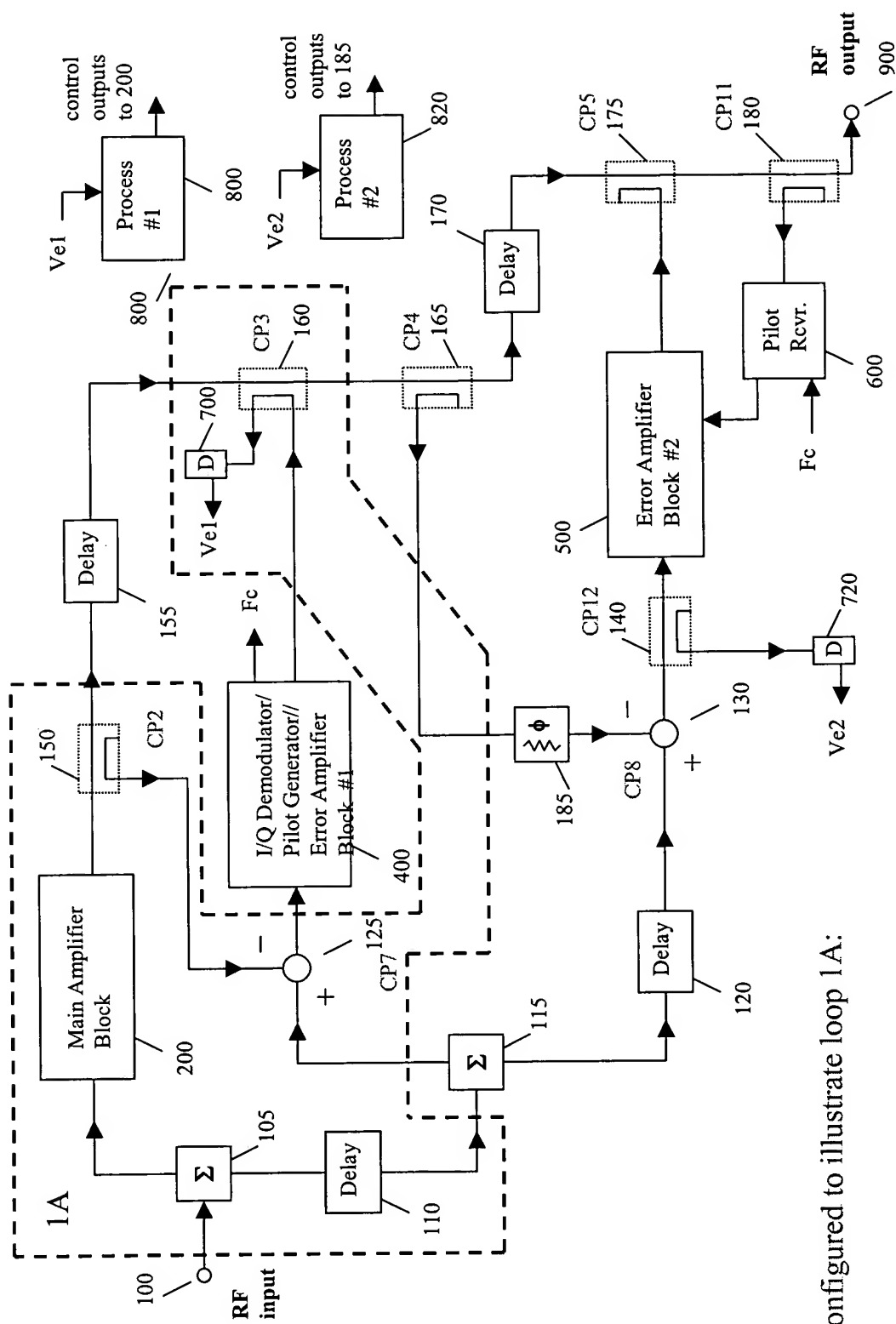
PRIOR ART

FIG. 2



PRIOR ART

FIG. 3

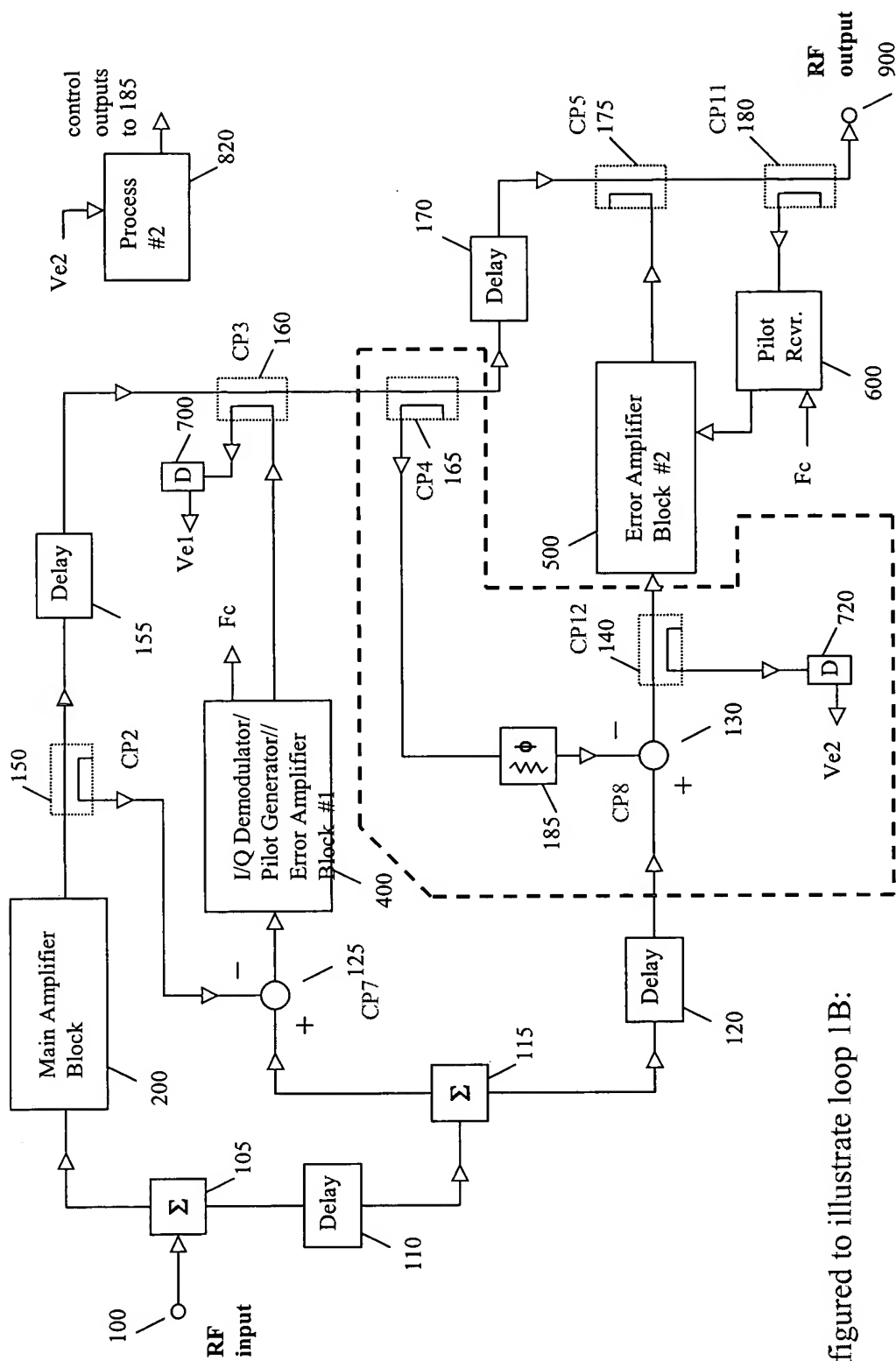


configured to illustrate loop 1A:

FIG. 4

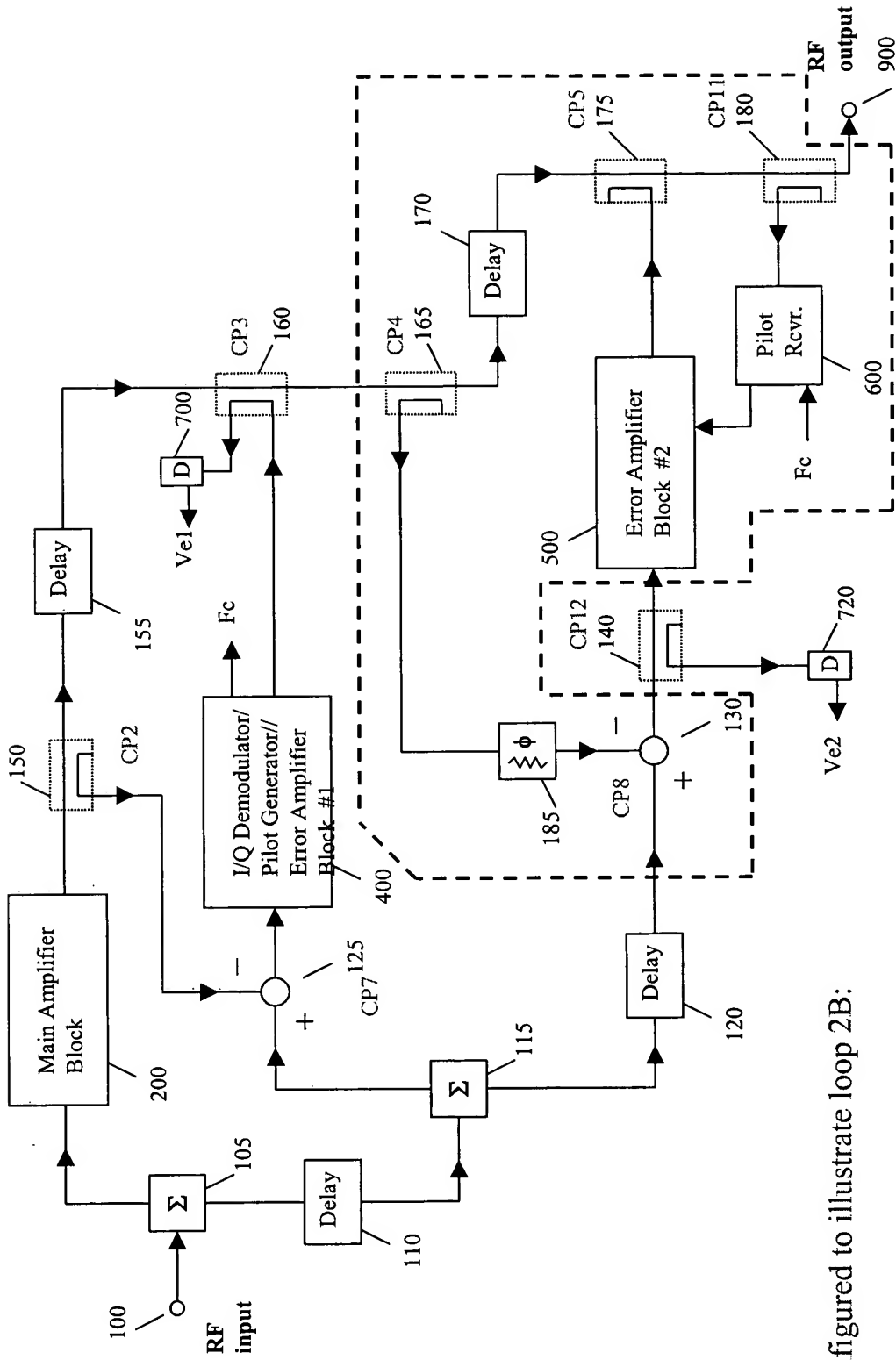


FIG. 5



configured to illustrate loop IB:

FIG. 6



configured to illustrate loop 2B:

FIG. 7

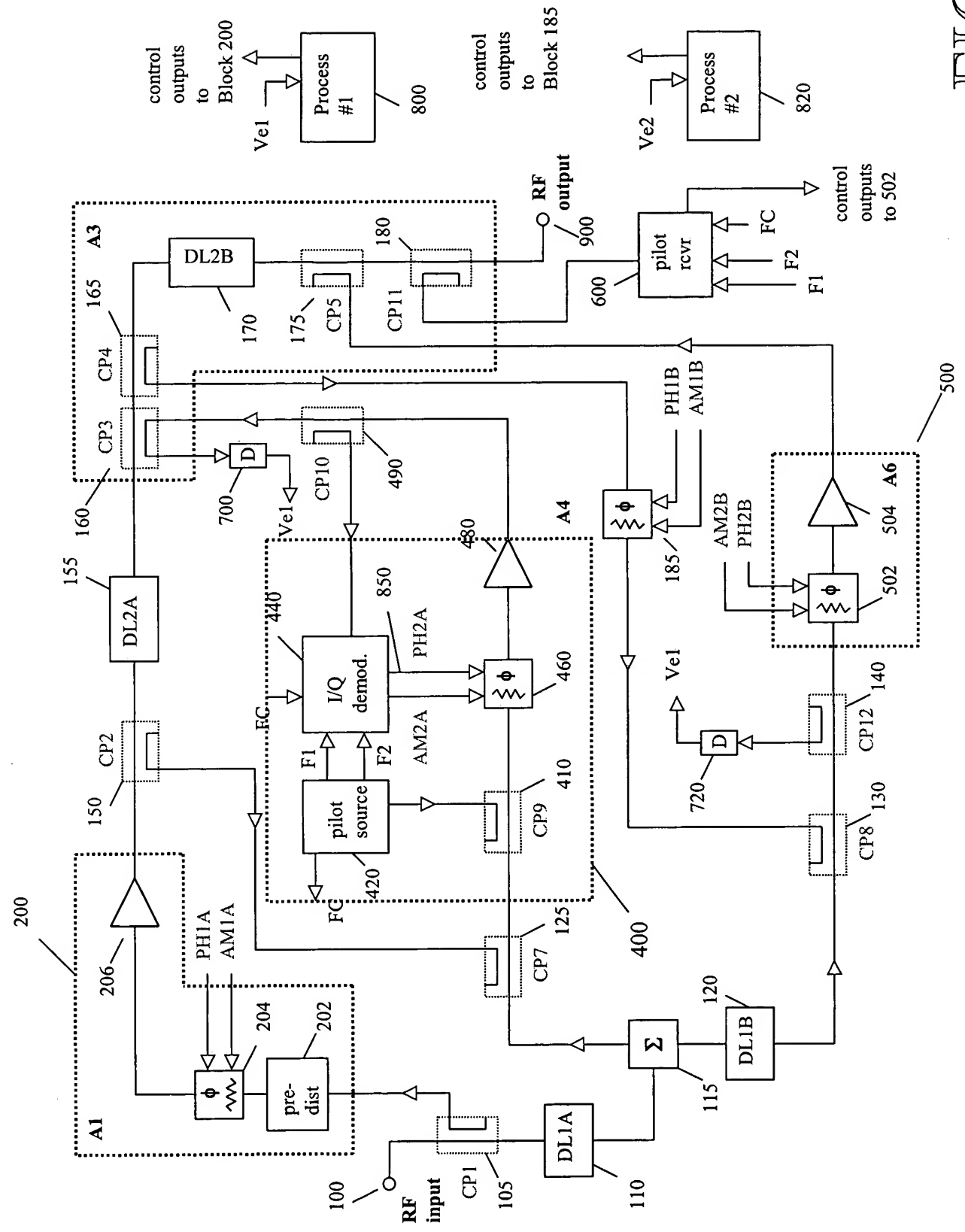


FIG. 8





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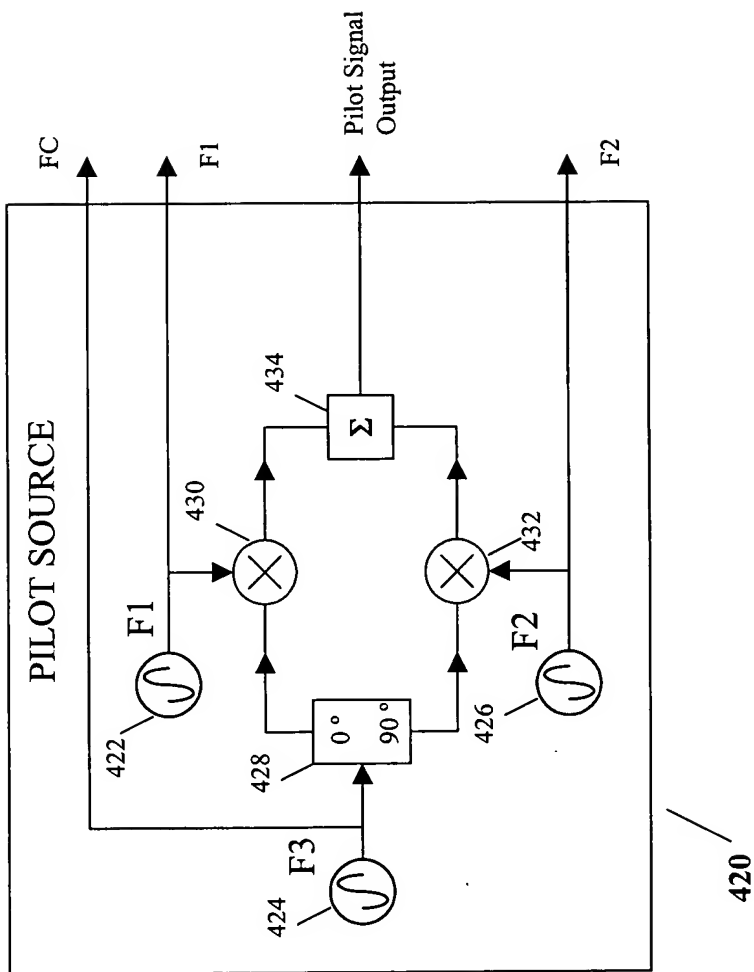


FIG. 9



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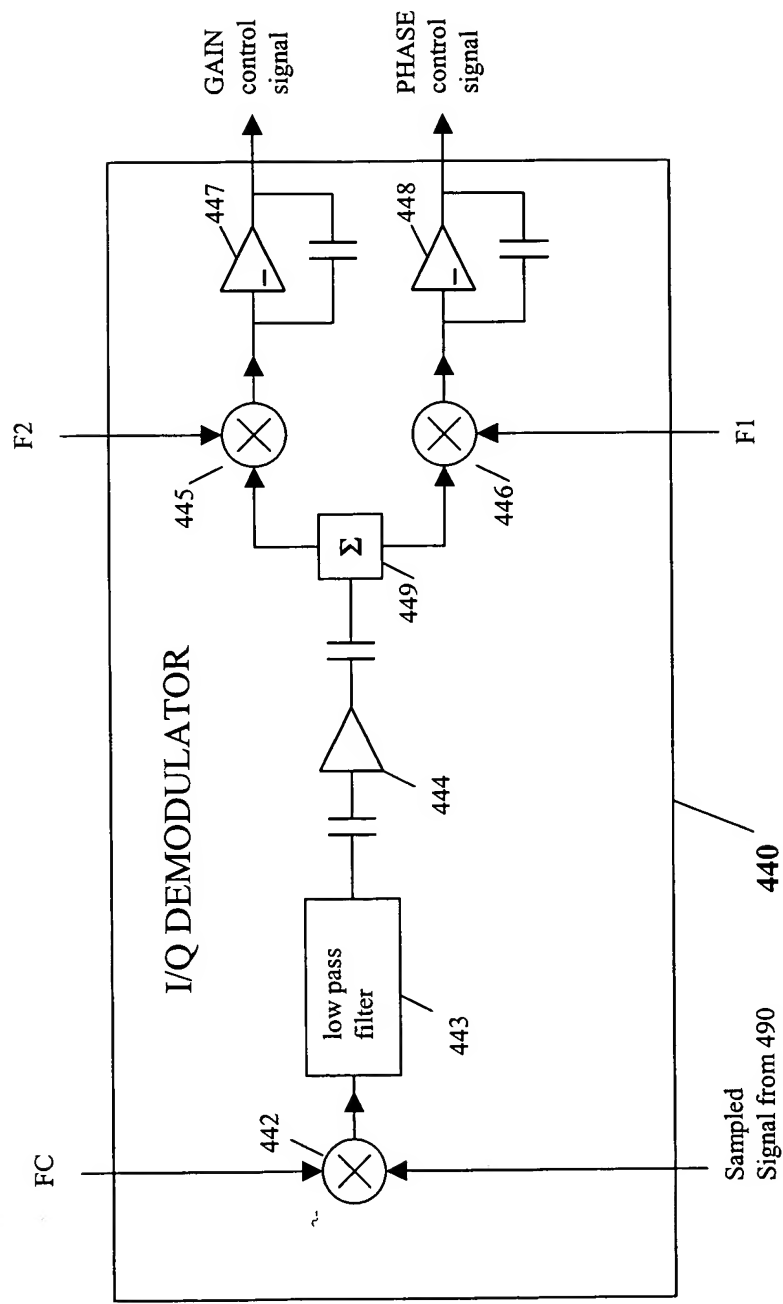


FIG. 10

Configured to illustrate output coupler loss minimization loop only:

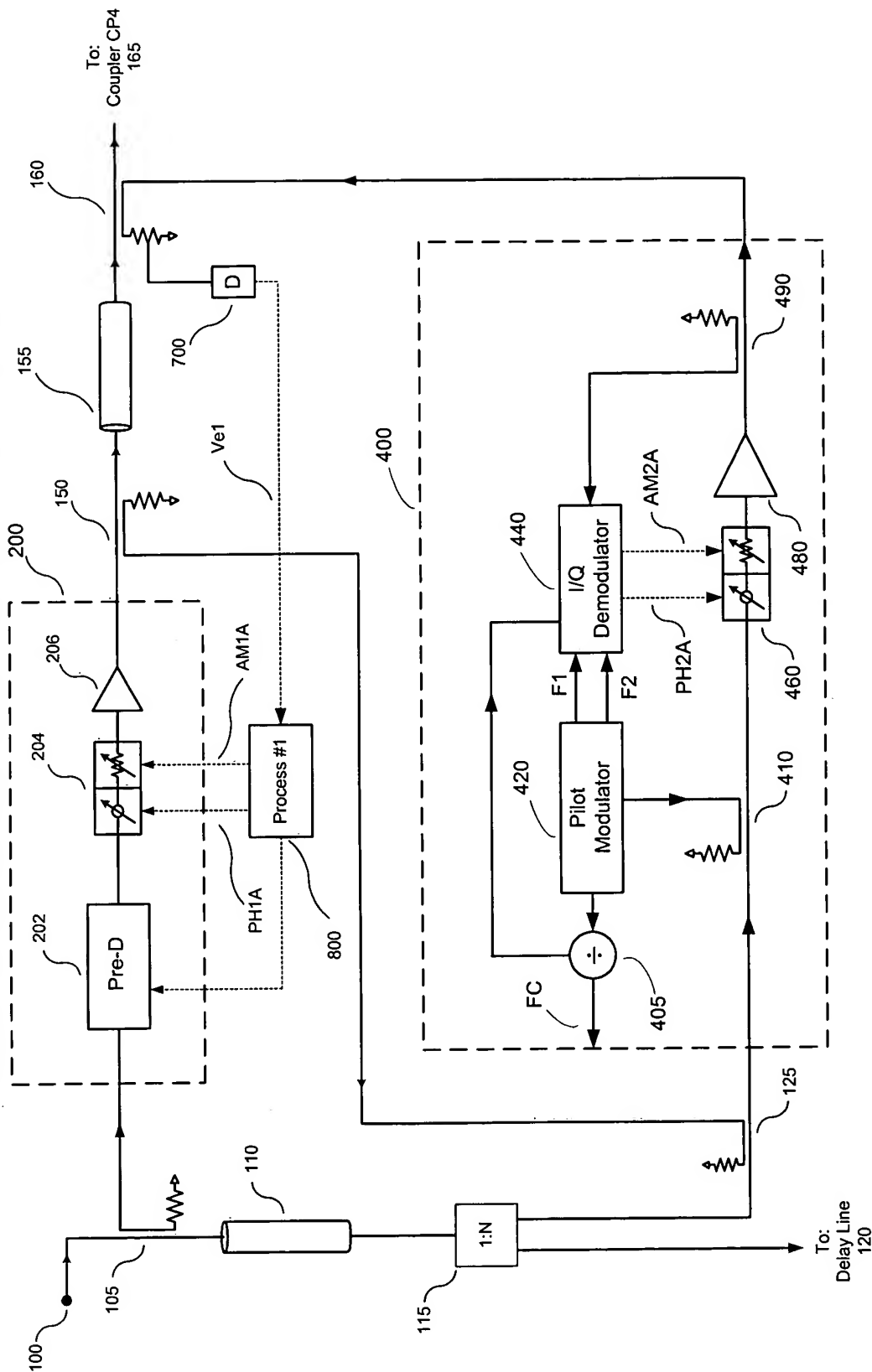


FIG. 11



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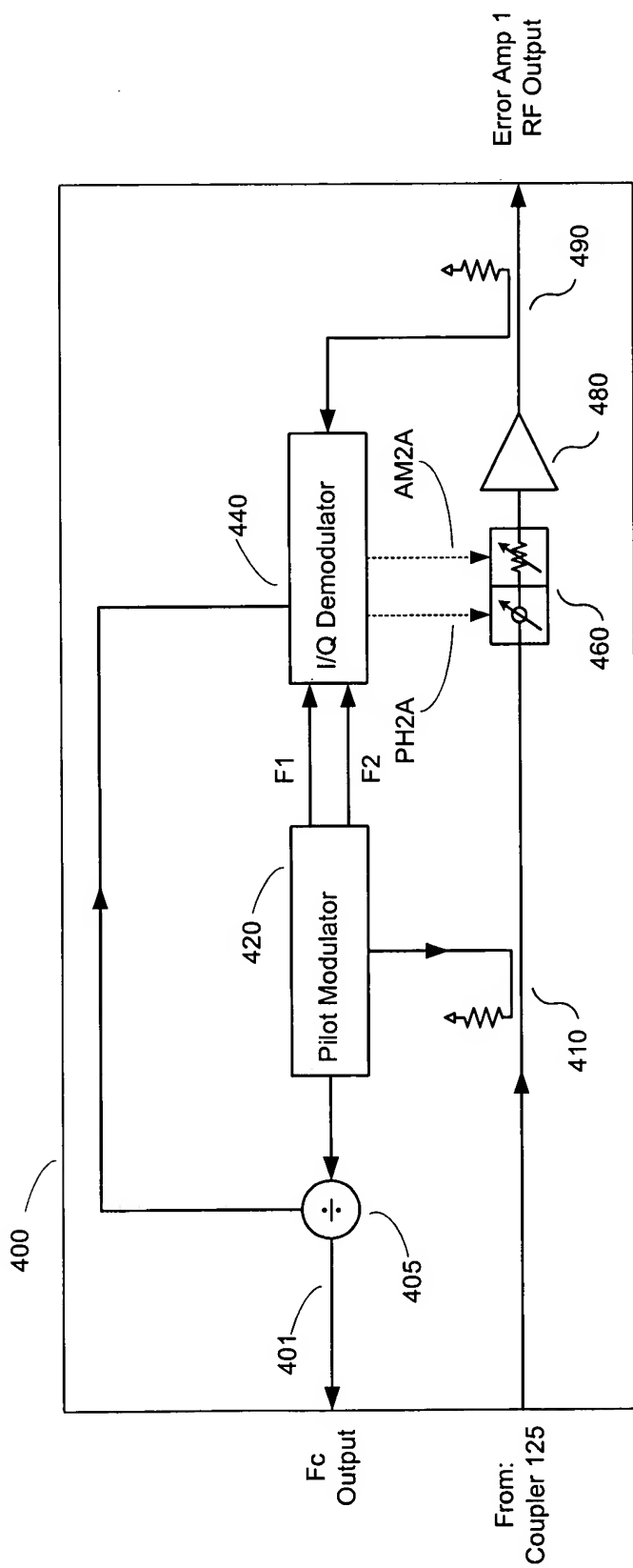


FIG. 12

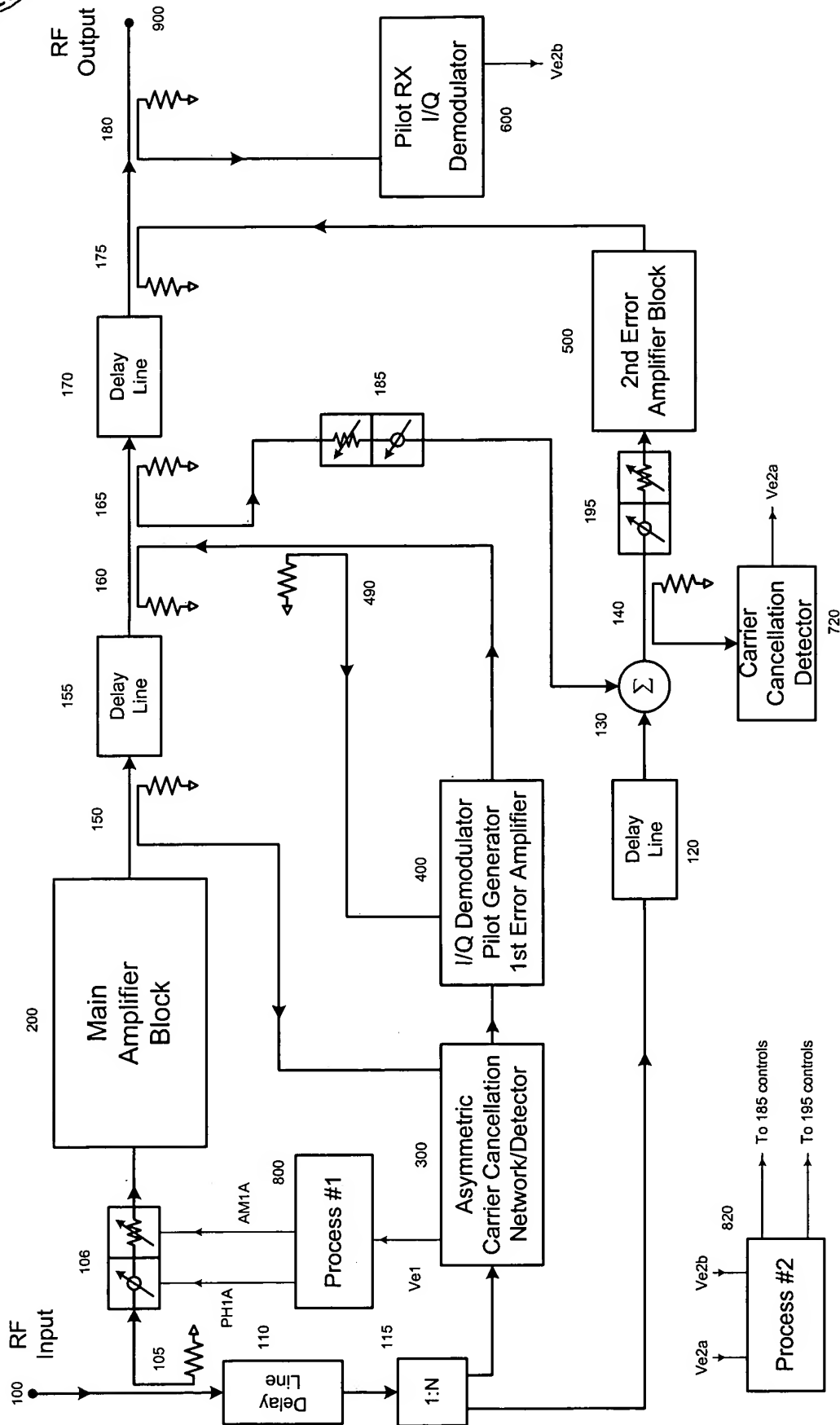


FIG. 13A

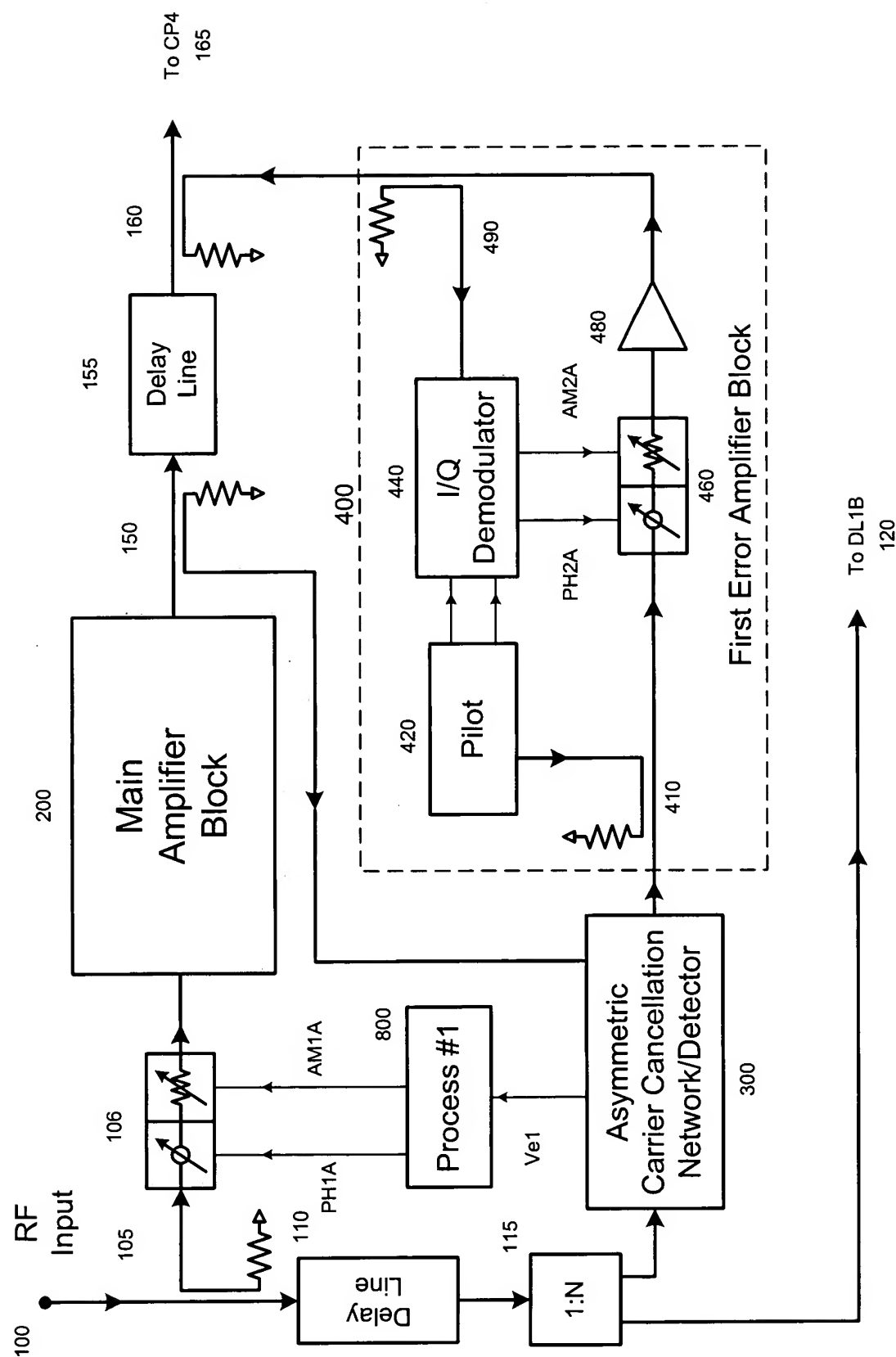


FIG. 13B

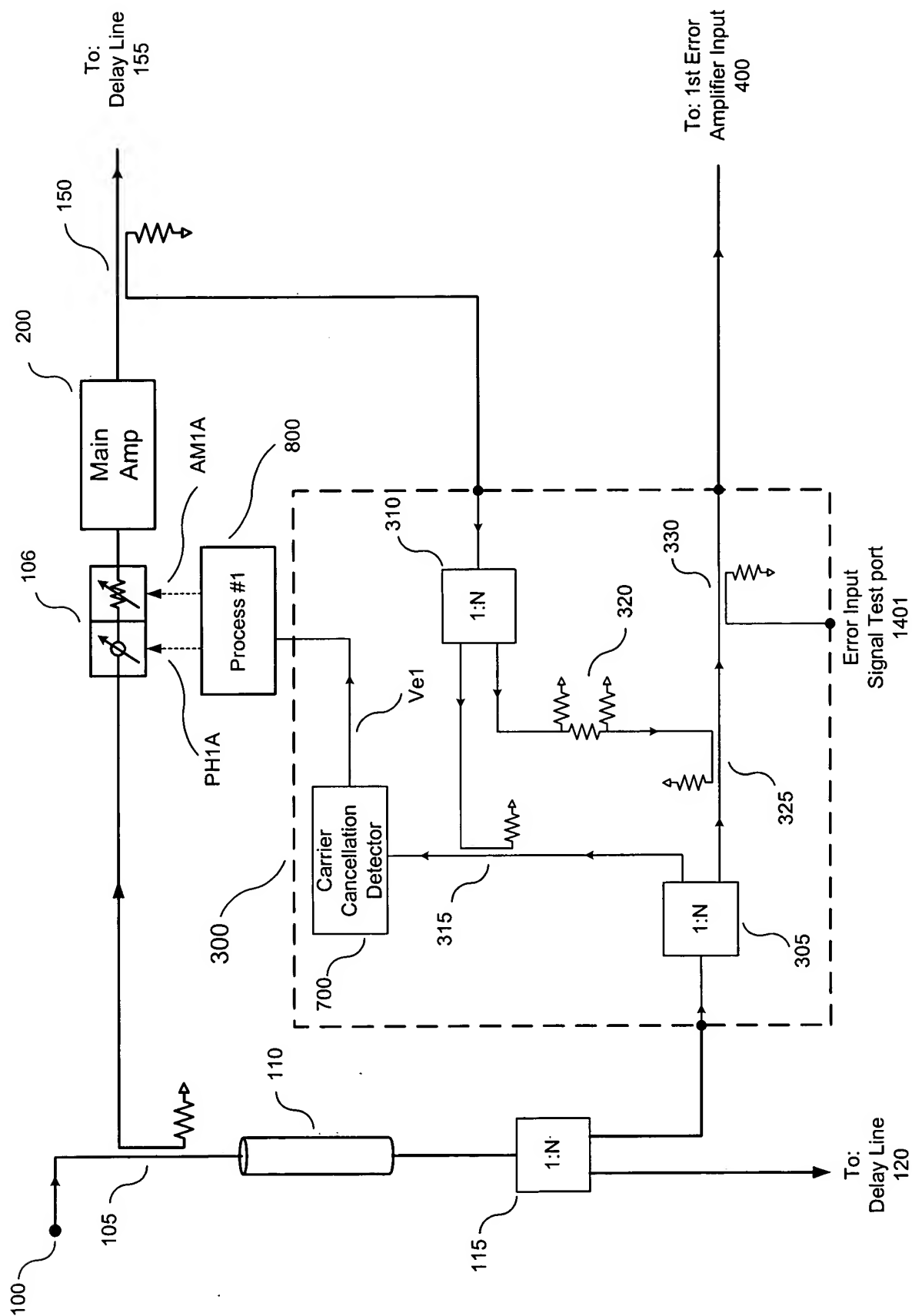


FIG. 14

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# 1<sup>st</sup> Error Amplifier Stabilization

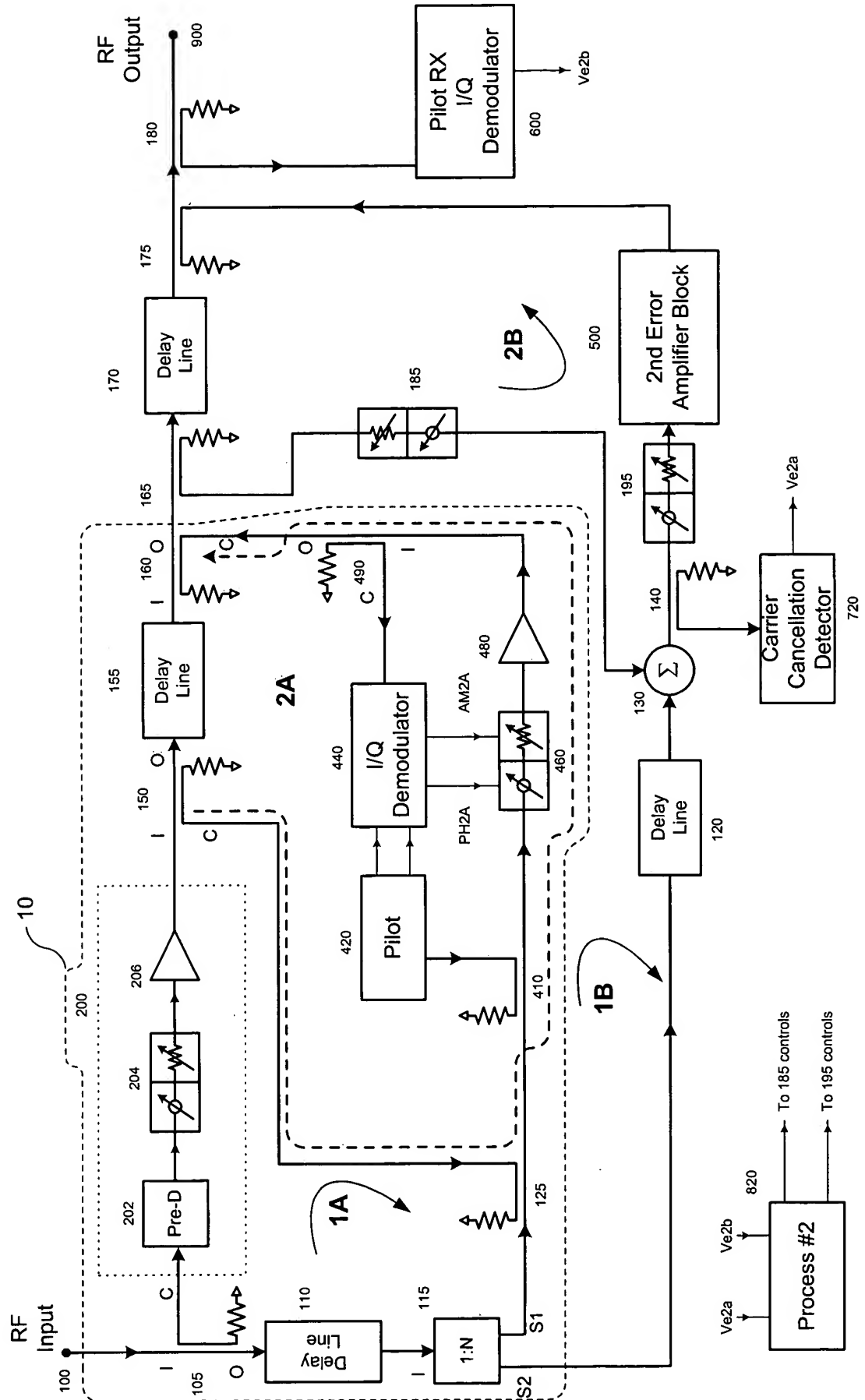


FIG. 15





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# Error Amplifier Stabilization

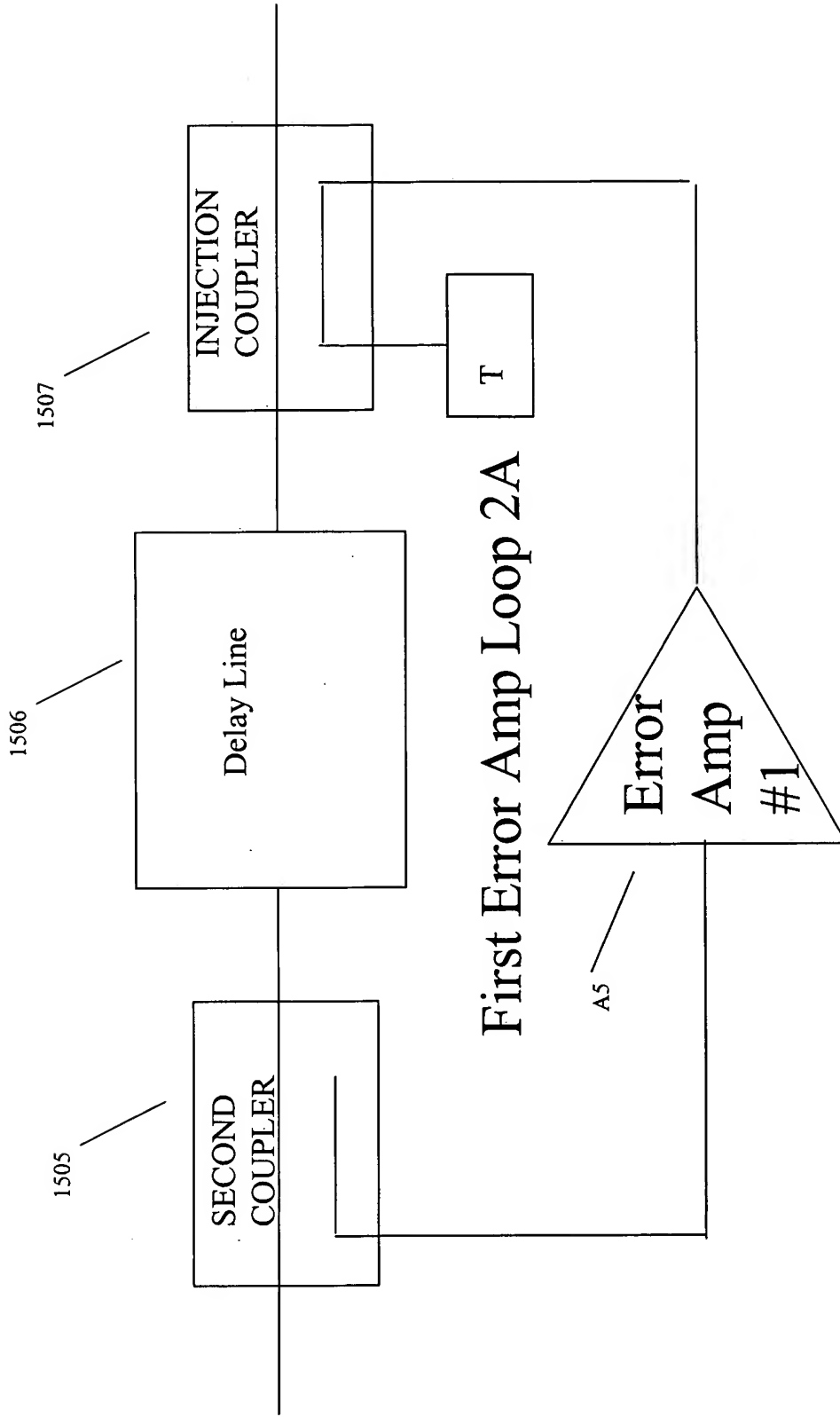


FIG. 16



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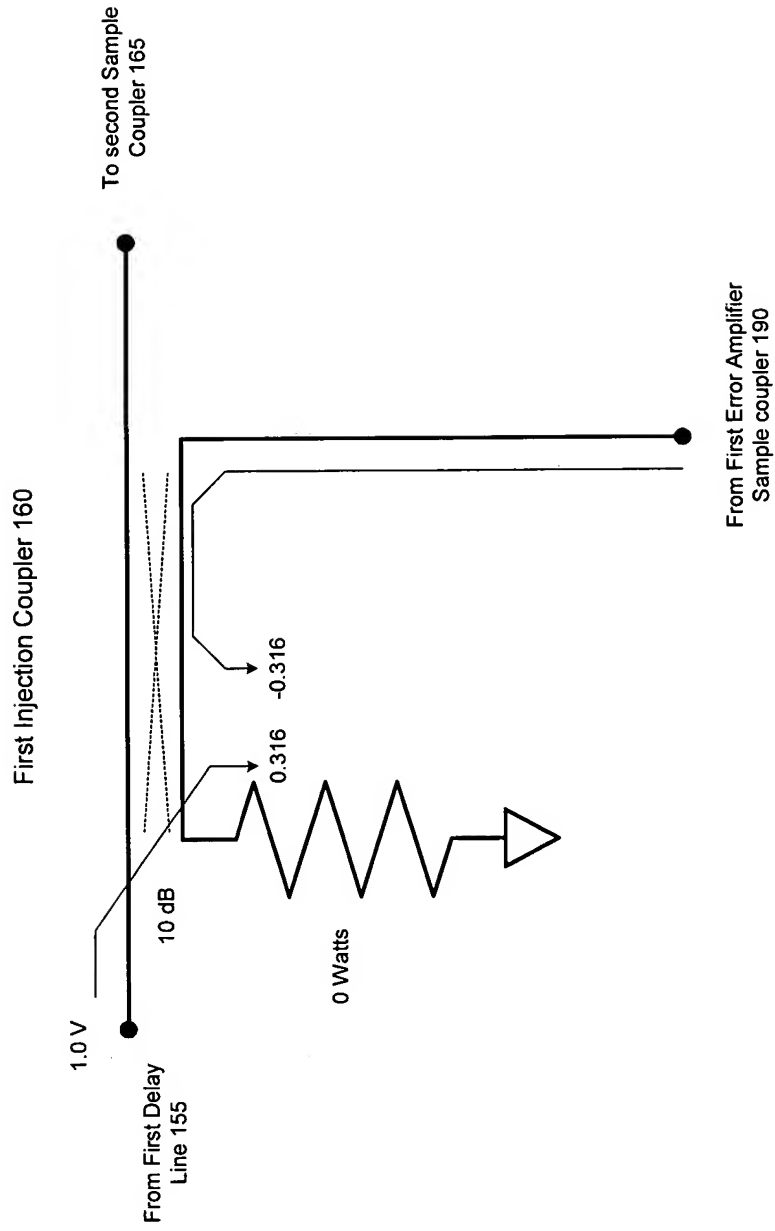
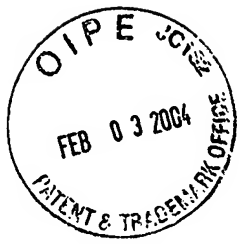


FIG. 17



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# Carrier Re-Injection

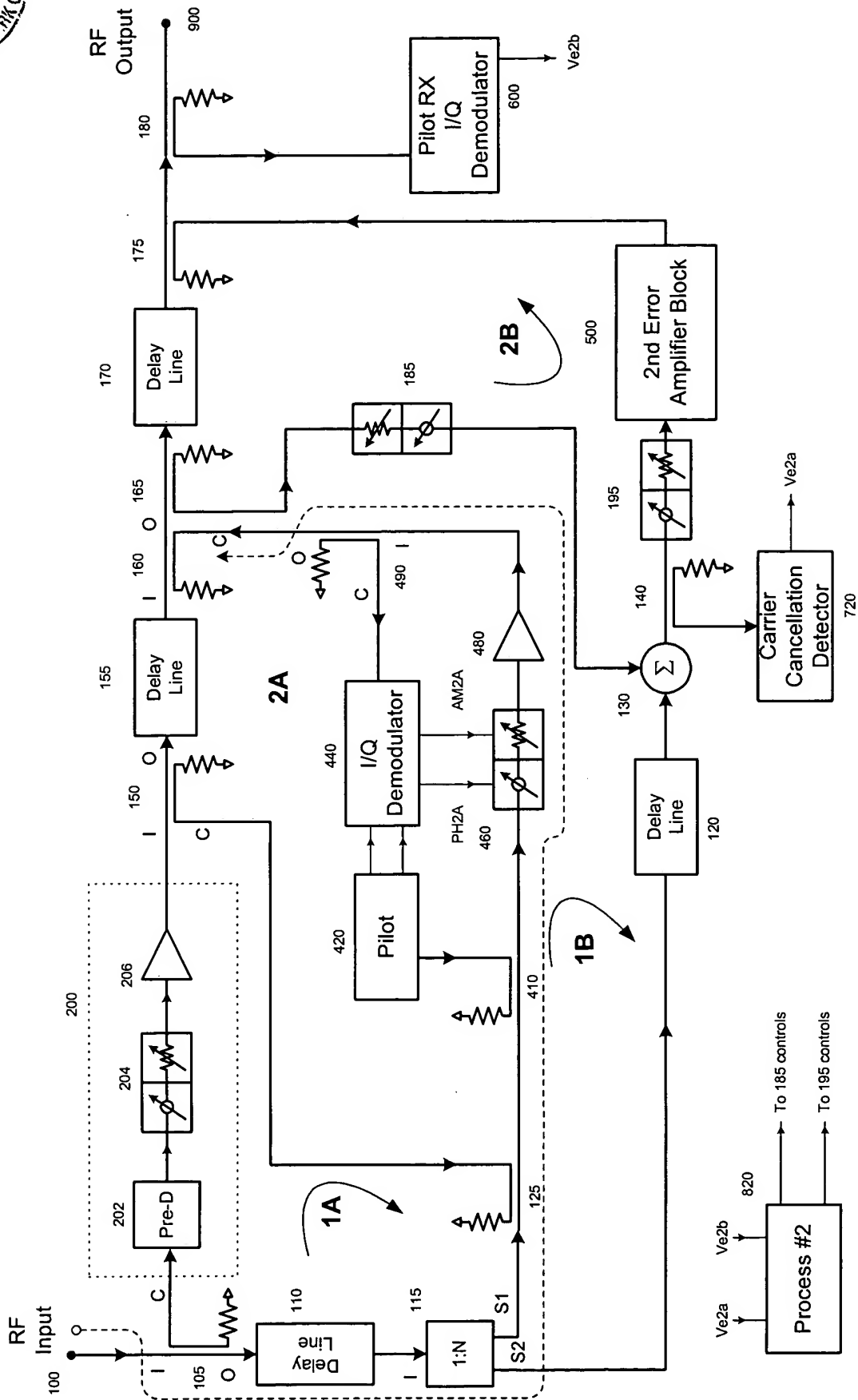


FIG. 18



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# Non-Overlapping Loop Adjustment

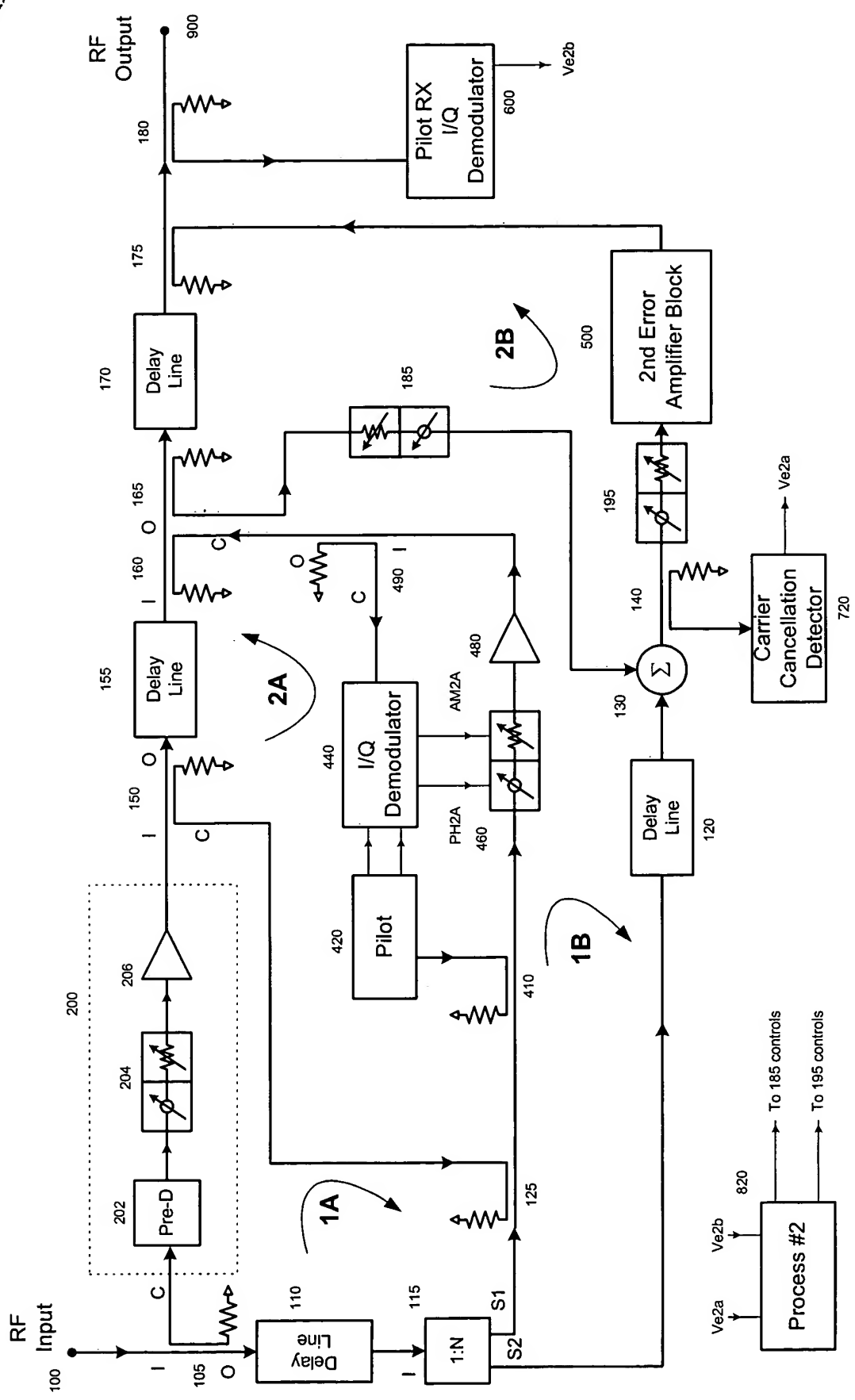


FIG. 19